

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: **Dow et al.**
Appl. No.: not yet assigned
Filed: on even date herewith
TC/A.U.: not yet assigned
Examiner: not yet assigned
Docket No.: 60999-168
Customer No.: **009629**
For: CDMA Power Amplifier Design For Low And High
Power Modes

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)

Sir:

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(b), Applicant brings to the attention of the Examiner the documents listed on the attached PTO-1449. This Information Disclosure Statement is being filed within three months of the filing date of the above-referenced application.

A copy of each listed document is attached except for U.S. Patents. Applicant respectfully requests that the Examiner consider the listed document and evidence that consideration by making appropriate notations on the attached form.

The relevance of each of the listed documents is discussed in the specification of the present application.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that the listed document is material or constitutes "prior art." If it should be determined that the listed document

does not constitute "prior art" under United States law, Applicants reserve the right to present to the Office the relevant facts and law regarding the appropriate status of such document. Applicants further reserve the right to take appropriate action to establish the patentability of the disclosed invention over the listed document, should the document be applied against the claims of the present application.

If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 50-0310.

Respectfully submitted,

Morgan, Lewis & Bockius LLP

Date: March 23, 2004



Steven K. Fukuda (Reg. No. 44,690)

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Attachments

INFORMATION DISCLOSURE CITATION (Use several sheets if necessary) PTO Form 1449	Attorney Docket No. 60999-168	Serial No. Unassigned
	Applicants Dow et al.	
	Filing Date on even date herewith	Group Unassigned

U.S. PATENT DOCUMENTS

*Examiner Initial	Document Number	Date	Name	Class	Sub Class	Filing Date
	5,774,017	06/30/1998	Adar			
	6,188,877	02/13/2001	Boesch et al.			
	6,215,359	04/10/2001	Peckham et al.			

FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Sub Class	<u>Translation</u>	
							YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)		
		Christopher B. Haskins, "Diode Predistortion Linearization for Power Amplifier RFICs in Digital Radios," thesis for Master of Science in Electrical Engineering, Virginia Polytechnic Institute and State University, April 17, 2000.

Examiner	Date Considered
Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	